

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-38605		SERIAL NO: Unknown			
		APPLICANT: Dye et al.					
		FILING DATE: Herewith		GROUP: Unknown			
FOREIGN PATENT DOCUMENTS							
EXAM. INITIAL S	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO N. YES/NO
/QC/		WO 94 10627 A	5/11/94	PCT			
/QC/		WO 94 15311 A	7/7/94	PCT			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
/QC/		De Coster, GRAPE-II: An Introduction [online]. Automatic Control and Computer Architectures Department. Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca					
/QC/		Collamati et al. "Induction Machine stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean Electrotechnical Conference, Vol. 1, pg. 495-498, May 1996.					
/QC/		Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. and Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997.					
/QC/		Srinivasan et al., "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. Of the 30th Southeastern Symp. On System Theory, pp. 520-524, March 1998.					
/QC/		Wahidanabanu et al., "Virtual Instrumentation with Graphical Programming for Enhanced Detection and Monitoring of Partial Discharges", Proc. Electrical Insulation Conf. 1997, pp. 291-296, September 1997.					
/QC/	B1	Choosing Block-Diagram Tools for DSP Design, http://www.bdti.com/articles/info_dspmt95blockdiagram.htm , May 9, 2003, pgs. 1-7.					
/QC/	B2	Real-Time Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.					
/QC/	B3	Guide to Rapid Prototyping with Simulink, Real-Time Workshop and dSPACE, 1995, 16 pages.					
/QC/	B4	Real-Time Interface to Simulink, RTI 30, User's Guide, 1995, 125 pages.					
/QC/	B5	Kevin J Gorman and Kourosh J. Rahnamai, "Real-Time Data Acquisition and Controls Using MatLAB", 1995, 4 pages.					
/QC/	B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.					
/QC/	B7	Signal Processing WorkSystem, MatLAB Interface User's Guide, Oct. 1995, 72 pages.					
/QC/	B8	Alta Group of Cadence Design Systems, Inc., 1995, 34 pages.					
/QC/	B9	Code Generation System Product Data Sheet, 1994, 8 pages.					

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/QC/	B10	SPW/CGS Porting Kits Product Data Sheet, 11/94, 2 pages.
/QC/	B11	MultiProx for SPW Product Data Sheet, 08/94, 4 pages.
/QC/	B12	DSP ProCoder for SPW Product Data Sheet, 11/94, 4 pages.
/QC/	B13	Xanalog Corporation Sales Manual, January 1987, 8 pages.
/QC/	B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.
/QC/	B15	Xanalog, XA-1000 Programming ICONS, 1986, 26 pages.
/QC/	B16	Xanalog's CAE System: The Fastest AT Alive, Mass High Tech, Vol. 4, No. 22, 08/1988, 1 page.
/QC/	B17	Xanalog The Computer Aided Engineering Workstation Comes to Simulation, Simulation Vol. 47, No. 1, July 1986, 3 pages.
/QC/	B18	Xanalog RT Real Time Analog and Digital I/O, 10/90, 4 pages.
/QC/	B19	Xanalog/SC+, 9/90, 4 pages.
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/QC/	B24	Gabriel 0.7 Overview, 1990, 5 pages.
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/QC/	B26	Edward A. Lee, "Design Methodology for DSP", 1992, 4 pages.
/QC/	B27	Pino et al., "Interface Synthesis in Heterogeneous System-Level DSP Design Tools, 05/1996, 4 pages.
/QC/	B28	Jose Luis Pino, Master's Report, "Software Synthesis for Single-Processor DSP Systems Using Ptolemy", May 1993, 48 pages.
/QC/	B29	Asawaree Kalavade and Edward A. Lee, "A Hardware-Software Codesign Methodology for DSP Applications", 1993, 12 pages.
/QC/	B30	Pino et al., "Automatic Code Generation for Heterogeneous Multiprocessors", 1994, 4 pages.
/QC/	B31	Tool Chest continues to Grow, Electronic Engineering Times, 12/15/1995, 2 pages.
/QC/	B32	Pino et al., "Mapping Multiple Independent Synchronous dataflow Graphs onto Heterogeneous Multiprocessors, 10/1994, 6 pages.
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/QC/	B34	Pino et al., "Software Synthesis for DSP Using Ptolemy", 1995, 15 pages.
/QC/	B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages.
/QC/	B36	i-Logix Product Overview, 1996, 52 pages.
/QC/	B37	Press Release, i-Logix Statemate MAGNUM Supports PCs", 01/31/1997, 2 pages.
/QC/	B38	Press Release, "i-Logix Signs Reseller Agreement for Virtual Prototypes, Inc.'s VAPS Product Line, 02/11/1997, 2 pages.
/QC/	B39	Press Release, "i-Logix Introduces Rhapsody, Object-Oriented analysis, Design and Implementation Tool", 02/10/1997, 2 pages.

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/QC/	B40	Statemate/C Product Overview, 1995, 4 pages.
/QC/	B41	Press Release, "i-Logix and Integrated Systems Link Statemate MAGNUM and MATRIX AutoCode" 01/03/1997, 2 pages
/QC/	B42	Press Release, "i-Logix and Wind River unveil Industry's First Rapid Prototyping Solution for Testing Embedded Systems at ESC West in San Jose, 09/17/1996, 3 pages.
/QC/	B43	Press Release, "i-Logix Inc. Endorses Unified Modeling Language, 01/16/1997, 1 page.
/QC/	D1	Philip Dean Lapsley, "Host Interface and Debugging of Dataflow DSP Systems", Thesis, 1991, pgs. 1-51.
/QC/	D2	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 1988, pgs. 1-11.
/QC/	D3	Lee et al., "Gabriel: A Design Environment for DSP", IEEE Transactions on Acoustics, Speech, and Signal Processing, Vol. 37, No. 11, Nov. 1989, pgs. 1751-1762.
/QC/	D4	Bier et al., "Gabriel: A Design Environment for DSP", IEEE Micro Issue - Vol. 10, No. 5, 1990, pgs. 28-45.
/QC/	U	Ludolph et al., "The Fabrik Programming Environment", IEEE, pages 222-230, 1988.
EXAMINER: /Qing Chen/		DATE CONSIDERED: 03/08/2007

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Information Disclosure Statement--PTO 1449 (modified)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/772,518
Confirmation No.:	5410
Filing Date:	February 5, 2004
First Named Inventor:	Robert E. Dye
Art Unit:	2122
Examiner Name:	Unknown
Attorney Docket Number:	5150-38605

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM- DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (<i>if known</i>)			
		US-			
		US-			
		US-			

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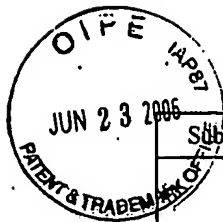
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		DE COSTER, GRAPE H. An Introduction [online]. Automatic Control and Computer Architectures Department, Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ DUP http://www.esat.kuleuven.ac.be/acca-	<input type="checkbox"/>
		COLLAMATI, et al. "Induction Machine stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean DUP Electrotechnical Conference, Vol. 1, pg. 495-498, May 1996.	<input type="checkbox"/>
		SPOELDER et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. and Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997. DUP	<input type="checkbox"/>
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Application Number 10/772,518
Confirmation No.: 5410
Filing Date: February 5, 2004
First Named Inventor: Robert E. Dye
Art Unit: 2122
Examiner Name: Unknown
Attorney Docket Number: 5150-38605

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		LUDOLPH et al., "The Fabril Programming Environment", IEEE, pages 222-230, 1988. DUP	<input type="checkbox"/>
		International Search Report for PCT/US 98/10916 dated 9/17/98. N/A	<input type="checkbox"/>
		DOUGLAS, BRUCE D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-637. N/A	<input type="checkbox"/>
		LADROSSE, JEAN, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214. N/A	<input type="checkbox"/>
		LADROSSE, JEAN, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389. N/A	<input type="checkbox"/>
		Operating Systems, 2nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415. N/A	<input type="checkbox"/>
		SHEARMAN, SAM, Assoc. Editor, "Hardware-in-the-loop connectivity extends continuous system simulation", Personal Engineering, June 1997, pp. 18-37. N/A	<input type="checkbox"/>
/QC/		XIAO-YU, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Hardware," Field Programmable Logic Architectures, Synthesis and Applications, 4 th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin, Germany, 1994.	<input type="checkbox"/>
/QC/		EDWARDS, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, vol. 143, no. 1, January 1996, pp. 55-63.	<input type="checkbox"/>
/QC/		LEESER, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, vol. 6, no. 2, August 1993, pp. 191-214.	<input type="checkbox"/>
/QC/		International Search Report for PCT/US 98/13040 mailed 12/22/98.	<input type="checkbox"/>
		LabVIEW USER MANUAL for Windows", National Instruments Co., 09/1994 pp. 4-1-21. N/A	<input type="checkbox"/>

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		DEQUELIN et al., "Visualization and Debugging in a Heterogeneous Environment", IEEE, 06/1993, pp 88-95. N/A	<input type="checkbox"/>
		JAMAL et al., "The Applicability of the Visual Programming Language LabVIEW to Large Real-World Application", IEEE, 1995, pp 99-106. N/A	<input type="checkbox"/>
		PER ARNE WIBERG, "Graphical Programming of Time-Deterministic Real-Time Systems", IEEE, 1996, pp 173-180. N/A	<input type="checkbox"/>
/QC/		ADE, M; LAUWEREINS, R; PEPERSTRAETE, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, 6/9/95, pp. 40-47.	<input type="checkbox"/>
/QC/		LAUWEREINS, R; ENGELS, M; ADE, M; PEPERSTRAETE, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, 2/95, pp. 35-43.	<input type="checkbox"/>
/QC/		WEBAN, A., BROWN, G. "A Software Development System for FPGA-based Data Acquisition Systems", FPGAs for Custom Computing Machines, 1996	<input type="checkbox"/>
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/QC/		SHAHEEN et al., "Remote Laboratory Experimentation", IEEE, Proceeding of American Control Conference, Pages: 1326-1329, June 1998	<input type="checkbox"/>
		GILLET et al., "Telepresence: An Opportunity to Develop Real-World Experimentation Education", CiteSeer, Pages: 1-6, 1997. N/A	<input type="checkbox"/>
		JOHNSTON et al., "High-Speed Distributed Data Handling for On-Line Instrumentation Systems", Lawrence Berkeley National Laboratory, pages 1-19, August 1997. N/A	<input type="checkbox"/>
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